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February 7, 2002

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**BOX PATENT APPLICATION** 

Commissioner for Patents Washington, D.C. 20231

Application of Shinya WATANABE, Hiroyuki KATOU

SEMICONDUCTOR CHIP HAVING AN ARRAYED WAVEGUIDE GRATING AND METHOD OF MANUFACTURING THE SEMICONDUCTOR CHIP AND MODULE

CONTAINING THE SEMICONDUCTOR CHIP

Assignee: NEC CORPORATION

Our Ref. Q67357

Dear Sir:

Attached hereto is the application identified above comprising 28 sheets of the specification, including the claims and abstract, 10 sheets of formal drawings, executed Assignment and PTO 1595 form, and executed Declaration and Power of Attorney.

The Government filing fee is calculated as follows:

Total claims	36 -	20	=	16	x	\$18.00	=	\$288.00
Independent claims	6 -	3	=	3	х	\$84.00	=	\$252.00
Rase Fee			<del></del>					\$740.00

TOTAL FILING FEE Recordation of Assignment TOTAL FEE

\$1280.00 \$40.00 \$1320.00

Checks for the statutory filing fee of \$1280.00 and Assignment recordation fee of \$40.00 are attached. You are also directed and authorized to charge or credit any difference or overpayment to Deposit Account No. 19-4880. The Commissioner is hereby authorized to charge any fees under 37 C.F.R. §§ 1.16 and 1.17 and any petitions for extension of time under 37 C.F.R. § 1.136 which may be required during the entire pendency of the application to Deposit Account No. 19-4880. A duplicate copy of this transmittal letter is attached.

Priority is claimed from:

Country

Application No

Filing Date

Japan

2001-046663

February 22, 2001

The priority document is enclosed herewith.

Respectfully submitted, SUGHRUE MION, PLLC

Attorneys for Applicant

Registration No. 24,625